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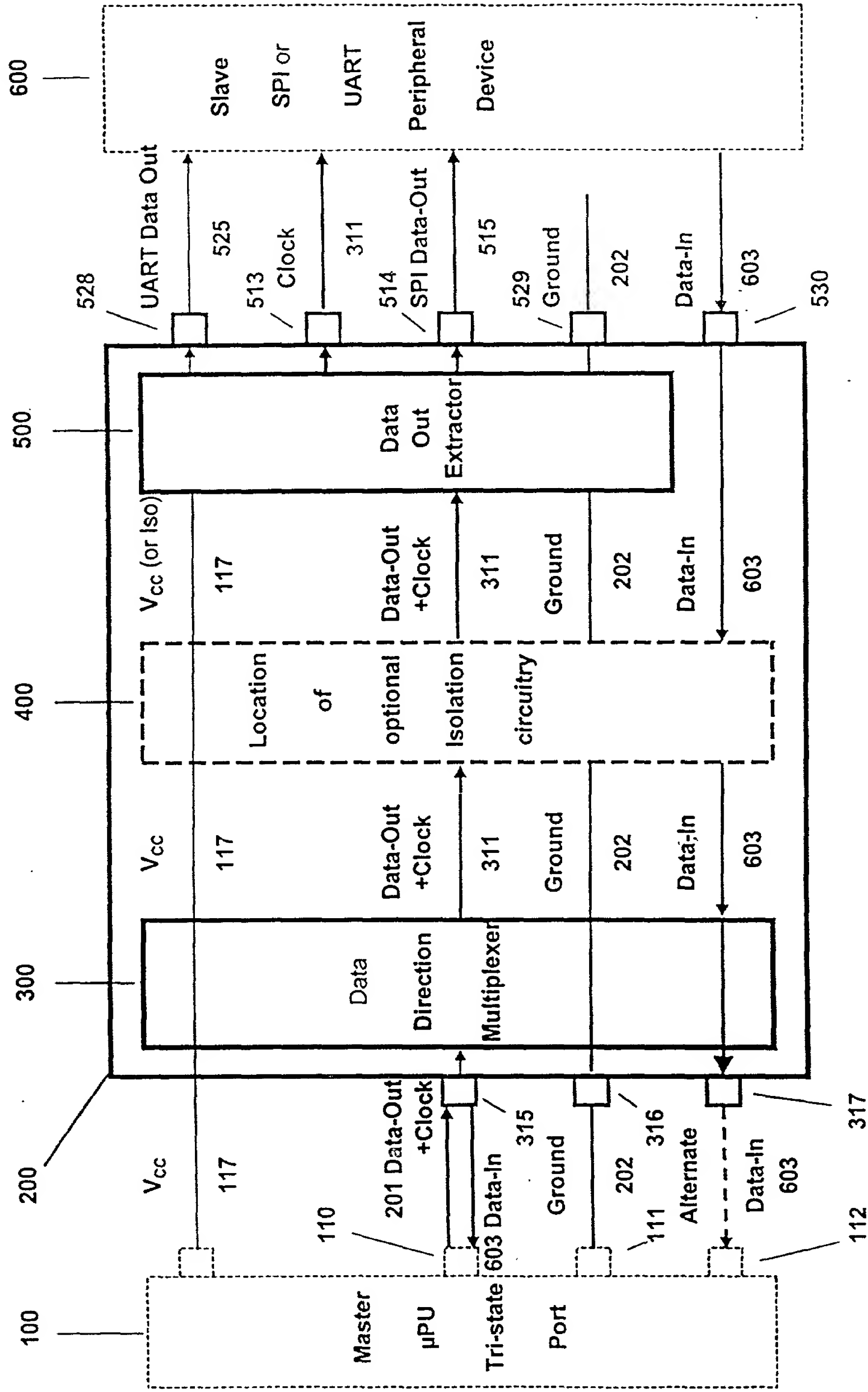


Figure 1 Monoline Serial Interface

Active Data-Out high 109

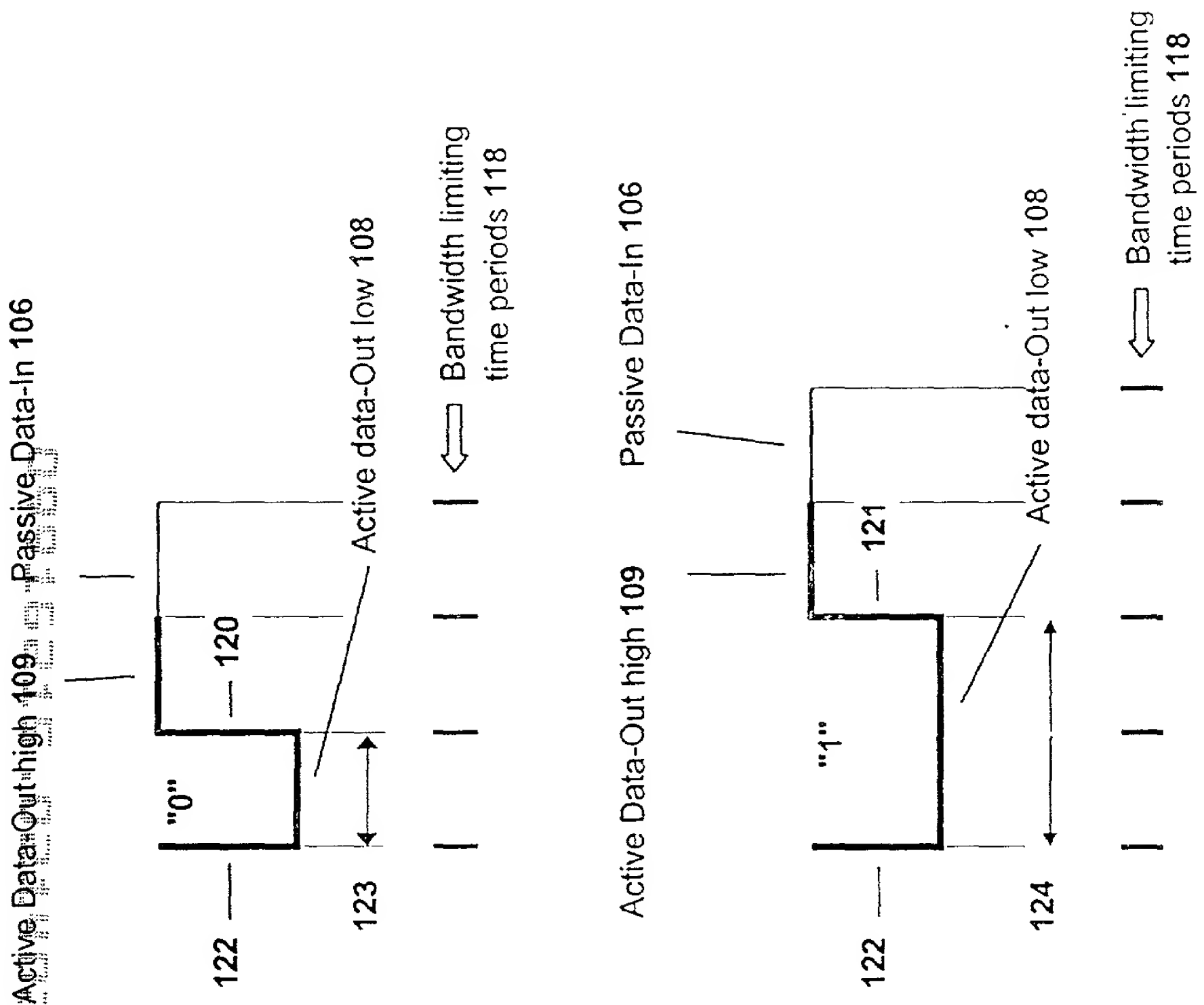


Figure 3

Microprocessor I/O Port Waveforms

Figure 6
"0" Data-Out bit

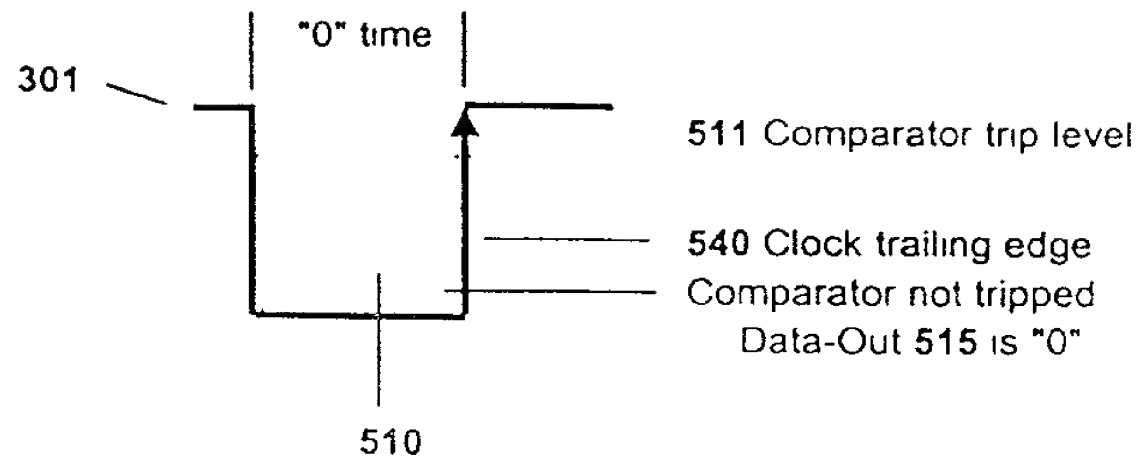
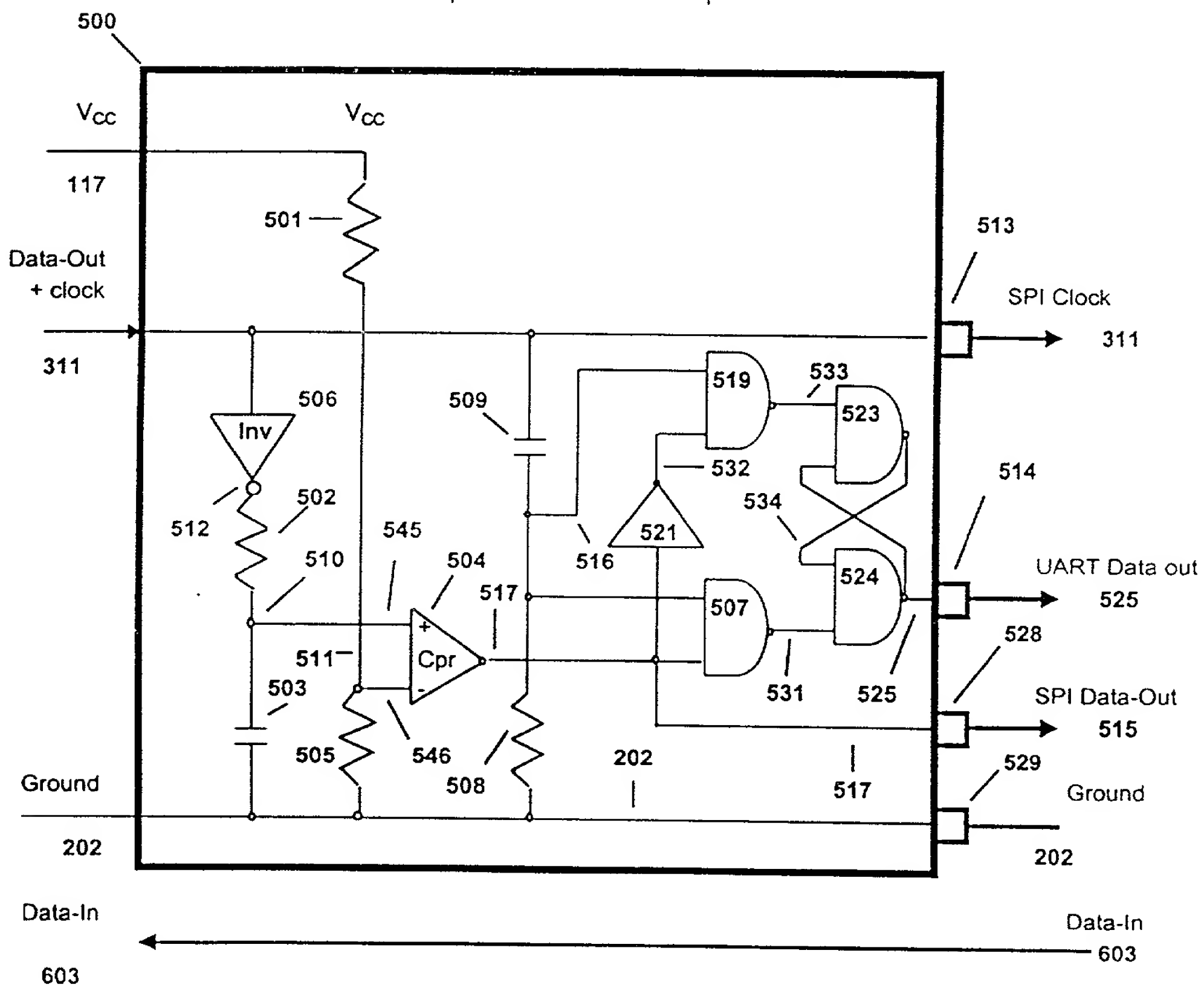
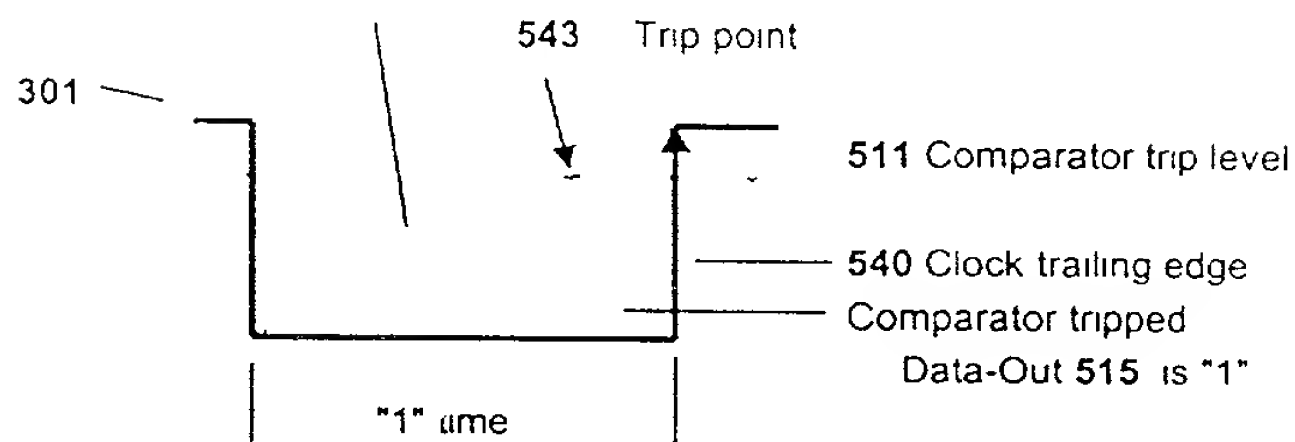


Figure 7
"1" Data-Out bit



Data-Out Extractor Circuit
Figure 8

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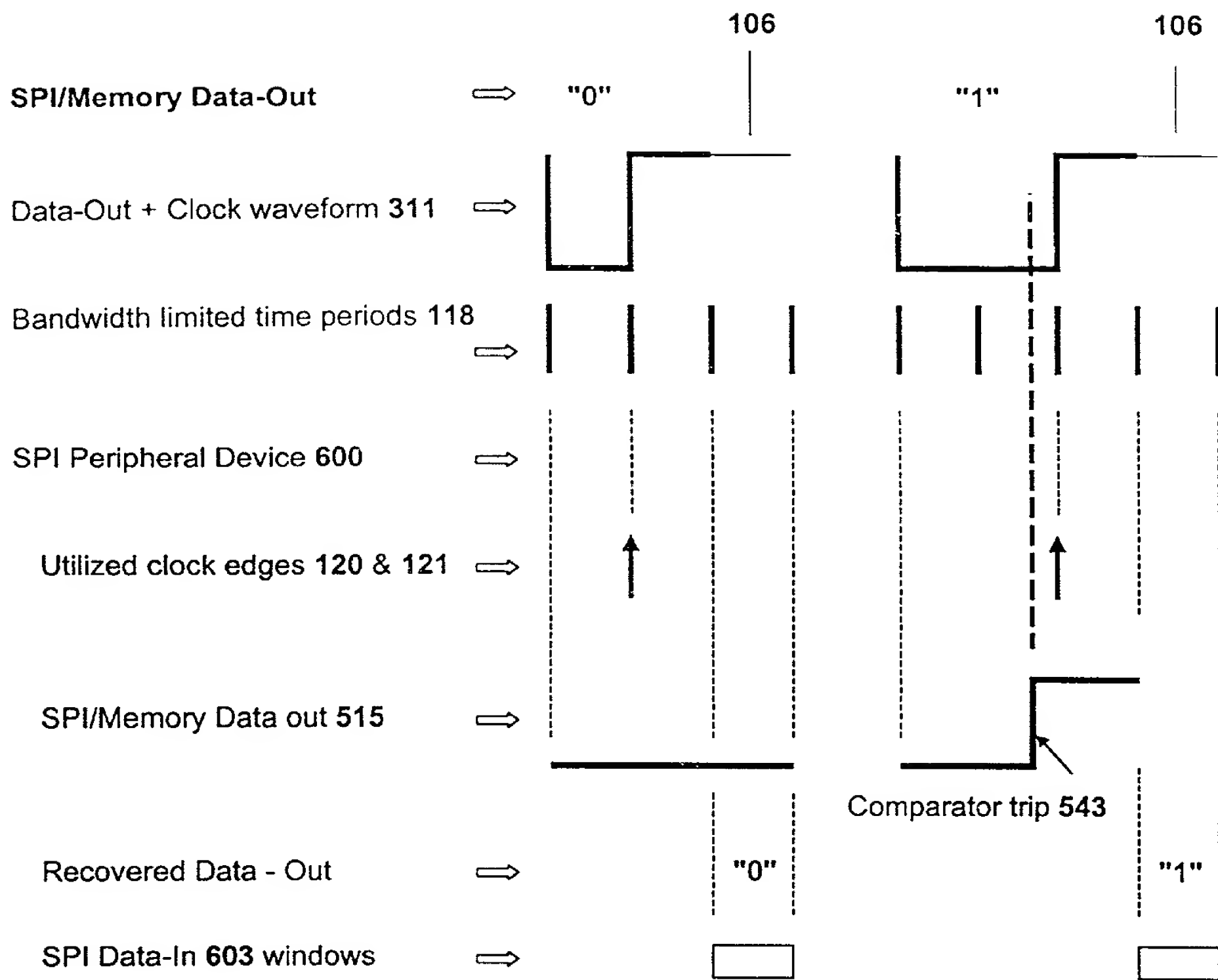


Figure 10 SPI Data

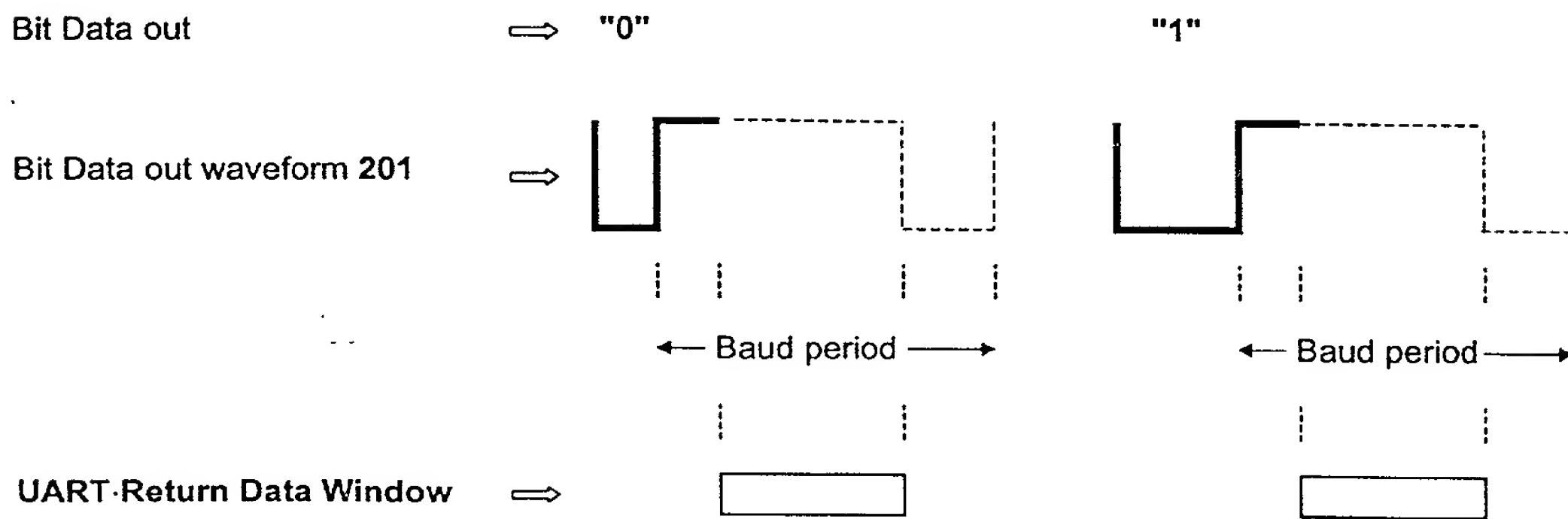


Figure 11 UART Data

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